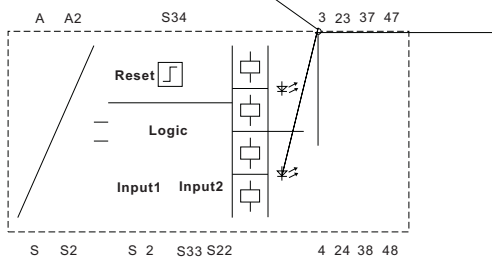
警告!

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/3


A A2
S34
3 23 37 47
Reset
Logic
Input1 Input2
S S2
S 2 S33 S22
4 24 38 48

警告!

( )



PLe Cat.4

PLd Cat.3

n \ ST \ t/s		0.1	1	3	5	7.5	10
1		0.1	1	3	5	7.5	10
2		0.2	2	6	10	15	20
4		0.4	4	12	20	30	40
8		0.8	8	24	40	60	80

警告!



ST LED

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